

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
29 April 2004 (29.04.2004)

PCT

(10) International Publication Number
WO 2004/036789 A1

(51) International Patent Classification⁷: **H04B 7/15**

(74) Agent: **POSZ, David, G.**; Posz & Bethards, PLC, 11250 Roger Bacon Drive, Suite 10, Reston, VA 20190 (US).

(21) International Application Number:
PCT/US2003/029130

(81) Designated States (*national*): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, OM, PH, PL, PT, RO, RU, SD, SE, SG, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VN, YU, ZA, ZM, ZW.

(22) International Filing Date: 15 October 2003 (15.10.2003)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
60/418,288 15 October 2002 (15.10.2002) US

(84) Designated States (*regional*): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

(71) Applicant (*for all designated States except US*): **WIDEFI, INC.** [US/US]; 476 Hwy A1A, Suite 3, Satellite Beach, FL 32937 (US).

(72) Inventors; and

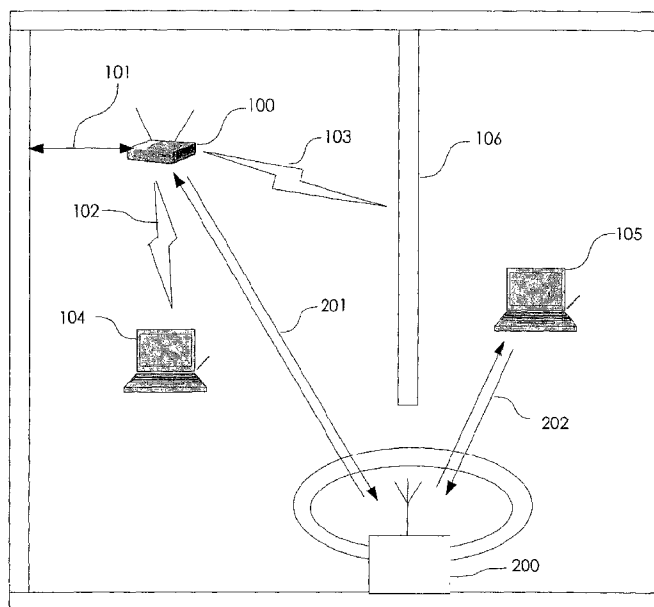
(75) Inventors/Applicants (*for US only*): **GAINEY, Kenneth, M.** [US/US]; Wifefi, Inc., 476 Hwy A1A, Suite 3, Satellite Beach, FL 32937 (US). **PROCTOR, James, A., Jr.** [US/US]; Wifefi, Inc., 476 Hwy A1A, Suite 3, Satellite Beach, FL 32937 (US).

Published:

— with international search report

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: WIRELESS LOCAL AREA NETWORK REPEATER WITH AUTOMATIC GAIN CONTROL FOR EXTENDING NETWORK COVERAGE



(57) Abstract: A frequency translating repeater (200) for use in a time division duplex radio protocol communications system includes an automatic gain control feature. Specifically, a received signal (330) is split to provide signal detection paths (331, 332) wherein detection is performed by amplifiers (301, 302) filters (311, 312), converters (313, 314) and a processor (315). Delay is added using analog circuits such as SAW filters (307, 308, 309, 310) and gain adjustment provided by gain control elements (303, 304, 305, 306).

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WIRELESS LOCAL AREA NETWORK REPEATER WITH AUTOMATIC GAIN
CONTROL FOR EXTENDING NETWORK COVERAGE

CROSS REFERENCE TO RELATED APPLICATIONS

[0001] This application is related to and claims priority from pending U.S. Provisional Application Number 60/418,288 filed October 15, 2002, and is further related to PCT Application PCT/US03/16208 entitled WIRELESS LOCAL AREA NETWORK REPEATER, the contents of which are incorporated herein by reference.

BACKGROUND OF THE INVENTION

[0002] The present invention relates generally to wireless local area networks (WLANs) and, particularly, the present invention relates to extending the coverage area associated with a WLAN repeater using Automatic Gain Control (AGC).

[0003] Several standard protocols for wireless local area networks, commonly referred to as WLANs, are becoming popular. These include protocols such as 802.11 (as set forth in the 802.11 wireless standards), home RF, and Bluetooth. The standard wireless protocol with the most commercial success to date is the 802.11b protocol although next generation protocols, such as 802.11g, are also gaining popularity.

[0004] While the specifications of products utilizing the above standard wireless protocols commonly indicate data rates on the order of, for example, 11 MBPS and ranges on the order of, for example, 100 meters, these performance levels are rarely, if ever, realized. Performance shortcomings between actual and specified performance levels have many causes including attenuation of the radiation paths of RF signals,

which for 802.11b are in the range of 2.4 GHz in an operating environment such as an indoor environment. Access point to client ranges are generally less than the coverage range required in a typical home, and may be as little as 10 to 15 meters. Further, in structures having split floor plans, such as ranch style or two story homes, or those constructed of materials capable of attenuating RF signals, areas in which wireless coverage is needed may be physically separated by distances outside of the range of, for example, an 802.11 protocol based system. Attenuation problems may be exacerbated in the presence of interference in the operating band, such as interference from other 2.4GHz devices or wideband interference with in-band energy. Still further, data rates of devices operating using the above standard wireless protocols are dependent on signal strength. As distances in the area of coverage increase, wireless system performance typically decreases. Lastly, the structure of the protocols themselves may affect the operational range.

[0005] Repeaters are commonly used in the mobile wireless industry to increase the range of wireless systems. However, problems and complications arise in that system receivers and transmitters may operate at the same frequency in a WLAN utilizing, for example, 802.11 WLAN or 802.16 WMAN wireless protocols. In such systems, when multiple transmitters operate simultaneously, as would be the case in repeater operation, difficulties arise. Typical WLAN protocols provide no defined receive and transmit periods and, thus, because random packets from each wireless network node are spontaneously generated and transmitted and are not temporally predictable, packet collisions may occur. Some remedies exist to address such difficulties, such as, for example, collision avoidance and random back-off protocols, which are used to avoid two or more nodes transmitting packets at the same time. Under 802.11

standard protocol, for example, a distributed coordination function (DCF) may be used for collision avoidance.

[0006] Such operation is significantly different than the operation of many other cellular repeater systems, such as those systems based on IS-136, IS-95 or IS-2000 standards, where the receive and transmit bands are separated by a duplexing frequency offset. Frequency division duplexing (FDD) operation simplifies repeater operation since conflicts associated with repeater operation, such as those arising in situations where the receiver and transmitter channels are on the same frequency for both the uplink and the downlink, are not present.

[0007] Other cellular mobile systems separate receive and transmit channels by time rather than by frequency and further utilize scheduled times for specific uplink/downlink transmissions. Such operation is commonly referred to as time division duplexing (TDD). Repeaters for these systems are more easily built, as the transmission and reception times are well known and are broadcast by a base station. Receivers and transmitters for these systems may be isolated by any number of means including physical separation, antenna patterns, or polarization isolation. Even for these systems, the cost and complexity of a repeater may be greatly reduced by not offering the known timing information that is broadcast, thus allowing for economically feasible repeaters.

[0008] Thus, WLAN repeaters operating on the same frequencies have unique constraints due to the above spontaneous transmission capabilities and therefore require a unique solution. Since these repeaters use the same frequency for receive

and transmit channels, some form of isolation must exist between the receive and transmit channels of the repeater. While some related systems such as, for example, CDMA systems used in wireless telephony, achieve channel isolation using sophisticated techniques such as directional antennas, physical separation of the receive and transmit antennas, or the like, such techniques are not practical for WLAN repeaters in many operating environments such as in the home where complicated hardware or lengthy cabling is not desirable or may be too costly.

[0009] One system, described in International Application No. PCT/US03/16208 and commonly owned by the assignee of the present application, resolves many of the above identified problems by providing a repeater which isolates receive and transmit channels using a frequency detection and translation method. The WLAN repeater described therein allows two WLAN units to communicate by translating packets associated with one device at a first frequency channel to a second frequency channel used by a second device. The direction associated with the translation or conversion, such as from the frequency associated with the first channel to the frequency associated with the second channel, or from the second channel to the first channel, depends upon a real time configuration of the repeater and the WLAN environment. The WLAN repeater may be configured to monitor both channels for transmissions and, when a transmission is detected, translate the received signal at the first frequency to the other channel, where it is transmitted at the second frequency.

[0010] The above described approach solves both the isolation issue and the spontaneous transmission problems as described above by monitoring and translating in response to packet transmissions and may further be implemented in a small

inexpensive unit. However, a WLAN repeater, in order to be legally compliant, must transmit within the power and spectrum limitations promulgated by, for example, the FCC. Difficulties arise however in that a received signal may have a widely varying power level requiring precise compensation for factors contributing to disruptions and failed or suboptimal signal retransmission caused by interference and the like.

SUMMARY OF THE INVENTION

[0011] Accordingly, in various exemplary and alternative exemplary embodiments, the present invention extends the coverage area in a wireless environment such as a WLAN environment, and, broadly speaking, in any time division duplex system including IEEE 802.16, IEEE 802.20 and TDS-CDMA, with a unique frequency detection and translation method. An exemplary WLAN frequency translating repeater allows two WLAN nodes or units to communicate by translating packets from a first frequency channel used by one device to a second frequency channel used by a second device. The direction of the conversion from channel 1 to channel 2, verses from channel 2 to Channel 1, is dependent upon real time configuration. The repeater may preferably monitor both channels for transmissions, and when a transmission on a channel is detected, the repeater is configured to translate the received signal to the other channel, where it is transmitted.

[0012] In a preferred embodiment, the signal received is detected on a first signal path and gain is applied on a second signal path. Further, the gain signal path preferably includes delay circuits to permit signal detection and gain setting to occur before the signal must be retransmitted. The gain is set based upon the detected receive power level to achieve a target transmit power level that is constant independent of the receive power level. However, the target power may be first determined or adjusted based upon criteria that includes one or more of the following: separation between receive and transmit frequencies, regulatory rule compliance, temperature, received power level, transmit power level and detected interference. A microprocessor with software, including calibration tables, is appropriate for performing the calculation of an appropriate gain set point, which fixes the target

output power. The details of this invention are described in detail in the figure descriptions that follow.

[0013] The preferred approach solves both the isolation issue, allowing a small inexpensive unit, and it solves the spontaneous transmission problem as it monitors and responds in reaction to the transmissions, with a constant output power at the transmitter. This output power may be different depending on the configuration of the repeater as determined by the microprocessor.

BRIEF DESCRIPTION OF THE DRAWINGS

[0014] FIG. 1 is a diagram illustrating a WLAN including an exemplary repeater having automatic gain control in accordance with various exemplary embodiments.

[0015] FIG. 2 is a schematic drawing illustrating an exemplary gain control interface unit of Figure 1.

DETAILED DESCRIPTION OF THE INVENTION

[0016] Referring now to FIG. 1, a wide area connection 101, which could be, for example, an Ethernet connection, a T1 line, a wideband wireless connection or any other electrical connection providing a data communications path, may be connected to a wireless gateway, or access point (AP) 100. The wireless gateway 100 sends RF signals, such as IEEE 802.11 packets or signals based upon Bluetooth, Hyperlan, or other wireless communication protocols, to client units 104, 105, which may be personal computers, personal digital assistants, or any other devices capable of communicating with other like devices through one of the above mentioned wireless protocols. Respective propagation, or RF, paths to each of the client units 104, 105 are shown as 102, 103.

[0017] While the signal carried over RF path 102 is of sufficient strength to maintain high-speed data packet communications between the client unit 104 and the wireless gateway 100, the signals carried over the RF path 103 and intended for the client unit 105 would be attenuated when passing through a structural barrier such as walls 106 or 107 to a point where few, if any, data packets are received in either direction if not for a wireless repeater 200, the structure and operation of which will now be described.

[0018] To enhance the coverage and/or communication data rate to the client unit 105, wireless repeater 200 receives packets transmitted on a first frequency channel 201 from the wireless gateway 100. The wireless repeater 200, which may be housed in an enclosure typically having dimensions of, for example, 2.5"x3.5"x.5", and which preferably is capable of being plugged into a standard electrical outlet and

operating on 110 V AC power, detects the presence of a packet on the first frequency channel 201, receives the packet and re-transmits the packet with more power on a second frequency channel 202. Unlike conventional WLAN operating protocols, the client unit 105 operates on the second frequency channel, even though the wireless gateway 100 operates on the first frequency channel. To perform the return packet operation, the wireless repeater 200 detects the presence of a transmitted packet on the second frequency channel 202 from the client unit 105, receives the packet on the second frequency channel 202, and re-transmits the packet on the first frequency channel 201. The wireless gateway 100 then receives the packet on the first frequency channel 201. In this way, the wireless repeater 200 is capable of simultaneously receiving and transmitting signals as well as extending the coverage and performance of the wireless gateway 100 to the client unit 105.

[0019] To address the difficulties posed by obstructions as described above and attendant attenuation of the signal strength along obstructed paths and thus to enhance the coverage and/or communication data rate to client unit 105, exemplary wireless repeater 200, as shown in FIG. 1, may be used to retransmit packets beyond a range limited by propagation path constraints through, for example, frequency translation. Packets transmitted on a first frequency channel 201 from AP 100 are received at repeater 200 and re-transmitted, preferably with a greater power level, on a second frequency channel 202. Client unit 105 preferably operates on second frequency channel 202 as if AP 100 were also operating on it, such as with no knowledge that AP 100 is really operating on first frequency channel 201 such that the frequency translation is transparent. To perform return packet operations, repeater unit 200 detects the presence of a transmitted return packet on second frequency channel 202

from client unit 105, and is preferably configured to receive the packet on second frequency channel 202, and to retransmit the data packet to, for example AP 100, on first frequency channel 201.

[0020] Wireless repeater 200 is preferably capable of receiving two different frequencies simultaneously, such as first frequency channel 201 and second frequency channel 202 determining which channel is carrying a signal associated with, for example, the transmission of a packet, translating from the original frequency channel to an alternative frequency channel and retransmitting the frequency translated version of the received signal on the alternative channel. Details of internal repeater operation may be found in co-pending PCT Application No. PCT/US03/16208.

[0021] Repeater 200 may thus receive and transmit packets at the same time on different frequency channels thereby extending the coverage and performance of the connection between AP 100 and client unit 105, and between peer-to-peer connections such as from one client unit to another client unit. When many units are isolated from one another, repeater unit 200 further acts as a wireless bridge allowing two different groups of units to communicate where optimum RF propagation and coverage or, in many cases, any RF propagation and coverage was not previously possible.

[0022] In accordance with various exemplary embodiments, repeater 200 is preferably configured to receive a signal and translate the frequency of the received signal with very little distortion or loss of the signal by properly controlling the gain of an exemplary transceiver section via Automatic Gain Control (AGC) circuitry 300

shown, for example, in FIG. 2. In a preferred embodiment, wireless repeater 200 shown is capable of receiving two different frequencies simultaneously, determining which one is present, translating the frequency of the one that is present to the other frequency and retransmitting a frequency translated version of the received signal.

[0023] In accordance with one preferred exemplary embodiment, AGC circuitry 300 utilizes RF delay and filter elements 307-310 to allow analog storage of an exemplary received waveform while signal detection and transmitter configuration takes place. It should be noted that signal detection may occur both prior to and during transit of signals in RF delay elements 307-310 providing time to perform system configuration. It should be noted that a detector power level is preferably used to set a gain value on a parallel signal path as part of the gain control operation.

[0024] Repeater AGC circuitry 300 further includes logarithmic amplifier 301 and 302, AGC control circuit 303 and 304, gain control element 305 and 306, which may preferably include variable gain or variable attenuator elements, and RF delay element 307-310 which may preferably include analog storage devices such as, for example, delay lines and/or band pass filters. Low pass filter 311 and 312, and analog to digital converter (ADC) 313 and 314 are further preferably used to accomplish gain control under the direction and control of, for example, microprocessor 315.

[0025] Since repeater 200, in accordance with various exemplary embodiments, is configured to simultaneously detect and process two different frequency signals, received signal 330 is split and propagated on two different RF paths, for example, using RF splitter 316. Likewise, because the two different frequency paths must be

delayed and controlled separately, each signal path is further split by, for example, IF Splitters 317 and 318. One of the split signal outputs from IF Splitter 317 is preferably coupled to logarithmic amplifiers 301 and the other split signal output is preferably coupled to gain control elements 305. Likewise, one of the split signal outputs from IF Splitter 318 is preferably coupled to logarithmic amplifiers 302 and the other split signal output is preferably coupled to gain control elements 306. The output of logarithmic amplifiers 301 is fed to AGC control circuit 303 and low pass filter 311. Likewise, the output of logarithmic amplifiers 302 is fed to AGC control circuit 304 and low pass filter 312. It should be noted that while logarithmic amplifiers 301 and 302 preferably provide an output voltage proportional to the logarithm of the power of received signal 330, tracking the envelope thereof, other devices known to those of ordinary skill in the art may also be used to track the envelope or samples of the envelope directly or proportionately.

[0026] The basic operation of components along the detection path of received signal 330 such as, for example, low pass filters 311 and 312, analog-to-digital converters (ADC) 313 and 314, and processor 315 for example, would be readily apparent to those of ordinary skill in the art and thus a detailed review of the basic operation thereof is omitted, such operation is disclosed in detail in commonly assigned co-pending PCT Patent Application No. PCT/US03/16208. However it should be briefly noted that processor 315 preferably detects the presence of an IF signal on detection paths DET1 331 and DET2 332. As described in the above identified co-pending application, signal detection may be based on the signal level exceeding a threshold using, for example, analog or digital signal comparison implements in processor 315, or could be performed by other means well known to

those of ordinary skill in the art. Once the signal is detected, gain control is applied to the signal using for example, AGC control circuits 303 and 304 on IF path IF1 333 or IF2 334 respectively, depending on the channel.

[0027] With reference still to FIG. 2 of the drawings, gain control is applied to signals on IF paths IF1 333 and IF2 334 using AGC control circuits 303 and 304 which circuits provide, *inter alia*, filtering of the analog voltage at the output of, for example, logarithmic amplifiers 301 and 302, any DC offset adjustment which may be necessary, AGC set point reference and control, level shifting/scaling, any required polarity reversal, and the like as would be appreciated by one of ordinary skill in the art. The output of AGC control circuits 303 and 304 are fed to gain control elements 305 and 306 which may provide either a djustable gain or a djustable attenuation of received signal 330 based on a value associated with, for example, the desired transmitter output power. It should be noted that AGC control circuits 303 and 304 may be one of a variety of gain control circuits, devices, or the like, as would be well known to those of ordinary skill in the art.

[0028] As an example of gain control in accordance with various exemplary embodiments, a variable attenuator could be used for gain control element 305 under the following conditions: desired output power +15dBm, received signal power -80dBm, total transceiver losses 65dB, total transceiver gains 165dB.

[0029] Under these conditions, a variable attenuator associated with, for example, gain control element 305, should be set according to the relation: Rx Signal Power - Desired Output Power + Total Gains - Total Losses, thus the attenuation would be -

80dBm – 15dBm +165dB – 65dB resulting in 5dB of attenuation. It will be appreciated that a voltage may be calculated and applied to the gain control element 305, for example, by AGC control circuit 303 resulting in the desired 5dB attenuation setting. It should also be noted that while AGC control circuit 303 and gain control element 305 are described herein, the above description applies to the operation of AGC control circuit 304 and gain control element 306.

[0030] Thus receive signal 330 in order to be retransmitted in accordance with various exemplary embodiments, and in accordance with the present example, is preferably output from gain control element 305 and delayed via Surface Acoustic Wave (SAW) filters 308 and 310. It will be appreciated that the delay introduced by SAW filters 308 and 310 acts to essentially store the analog waveform while AGC and signal detection processes, for example as described above, are carried out, meaning that detection and gain control setting are preferably completed during the propagation interval of the signal.

[0031] In accordance with various exemplary and preferred exemplary embodiments, RF delays are imposed through SAW filters 307-310 enabling analog signal storage and channel selection, jammer suppression, and a feed-forward variable gain control path. AGC control circuits 303 and 304 and gain control elements 305 and 306 may be biased or otherwise set under control of for example processor 315, which is preferably a micro-processor, such as a general purpose processor, dedicated processor, signal processing processor, or the like as would be understood by one of ordinary skill in the art. Further, set points may be obtained by processor 315 from a look up table or the like depending on which channel received signal 330 is received

on and which channel is selected for signal retransmission. It should be noted that different bands have different transmit power limitations in different countries, thus the selection of gain set points may be driven by several factors resulting from the need to meet FCC requirements and related specifications for the desired band such as spectral re-growth and Effective Isotropic Radiated Power (EIRP).

[0032] After detection and setting of the gain control, IF Switch 319 and LO Switch 320 are preferably set to retransmit received signal 330 at a different frequency without significantly cutting off the waveform preamble. It is important to note that detection and power sensing, for example, as described above, is preferably performed on detector paths DET1 331 and DET2 332, but actual gain control may be applied the on IF paths IF1 333 and IF2 334. More specifically referring again to FIG. 2, outputs from the logarithmic amplifiers 301 and 302 are fed to AGC control circuits 303 and 304 which circuits are making adjustments either as variable gain or attenuation with regard to gain control elements 305 and 306.

[0033] One factor in determining a sequence of signal detection and gain control is the effect caused by splitting the output voltage from logarithmic amplifiers 301 and 302 into a signal detection path and a gain control path, each having potentially two different filter bandwidths. As can be noted from FIG. 2, the gain control path is the path to AGC control circuits 303 and 304, while the signal detection path is the path leading to low pass filters 311 and 312, as previously described. Thus, if desired, the AGC control values and the signal detection filter bandwidth could be set differently. For example, the AGC control loop could be set to react very quickly to the incoming power envelop while signal detection, as carried out, for example, in ADC 313 and

314 and processor 315, could be configured to react more slowly. As a result, received signal 330 propagating in gain control elements 305 and 306 can be tracked very accurately while the portion of received signal 330 propagating in ADC 313 and 314 and processor 315 may track more slowly, but with more detection process gain.

[0034] It should be noted that in accordance with various exemplary and preferred exemplary embodiments, two separate detectors are used for performing detection of the presence of received signal 330 and for detection of the power level thereof in order to set gain. Thus, since signal detection may occur more slowly than AGC as described, different signal detection and AGC filter bandwidths may be used beneficially, allowing variable control elements associated with AGC such as gain control elements 305 and 306 to have a faster or slower response than the output of filters 311 and 312.

[0035] Another factor in controlling gain is the relative distance between the receive and transmit channels. Specifically, depending on the distance therebetween, the target output power or set point from the gain control elements 305 and 306 can be different to the extent that additional performance may be gained when the receive and transmit channels are further apart in frequency. Gain values may be increased in gain control elements 305 and 306 while continuing to meet performance requirements. Further, AGC control circuits 303 and 304 may be programmed to increase power based on the frequency difference or, alternatively, processor 315 may be programmed to control AGC control circuits 303 and 304 based on frequency separation. Adjusting set points based on frequency separation may further include

applying more filtering to any leakage signals picked up by a receiver to avoid self interference.

[0036] A factor affecting the choice of which channels to operate on during initial repeater power up may be influenced by choosing repeating channels based on the ability to transmit more power in different FCC bands or bands controlled by other regulatory bodies. For example, in the U-NII bands for operation in the United States, the maximum allowable transmit power for CH36-48 is 50mW, for CH52-64 is 250mW, and for CH149 – 161 is 1W. Therefore it is possible to receive a signal in on a channel associated with one of the lower power bands and choose a channel on a different band allowing higher transmit power, thereby allowing a higher AGC set point. Thus the set points for a translation, say from F1 to F2 and F2 to F1 would be different. The decision of which channels to select is preferably pre-programmed during manufacturing, or, alternatively could be programmed in the field, in, for example, AGC control circuits 303 and 304 or processor 315.

[0037] In accordance with other aspects of the present invention, gain control may require AGC calibration during initial manufacturing. Calibration may be desirable to allow the use of lower tolerance parts thus reducing cost. Calibration may further provide for accuracy required for regional or band specific power settings. Accordingly, calibration may include setting up circuits and devices in accordance with one or more of the following; regional regulatory rules, frequency channel, received power level, transmit power level, temperature, and the like. In accordance with various exemplary and preferred exemplary embodiments, repeater 200 using, for example, processor 315, may store calibration tables and the like and be

configured, for example through the use of software, programs, instructions or the like, to pass specific calibration values to AGC control circuits 305 and 306. Processor 315 would preferably utilize a digital to analog conversion process to control the set point.

[0038] As mentioned above, different detector outputs may be used for AGC and signal detection. Signal detection may be performed in an analog only configuration using, for example, a threshold comparator under the control of processor 315 which may be configured to actively control, for example, an analog reference voltage a threshold comparator uses to make the detection decision. Alternatively, received signal 330 may be digitized and a detection decision made, for example, in processor 315. One concern related to using a digital path and processor 315 includes delay associated with, for example, digital sampling and decision making instructions in a processor 315.

[0039] In accordance with various alternative exemplary embodiments an analog comparator (not shown) having a threshold controlled by processor 315 may be used. Such a configuration could be equipped with a digital override to allow for a fast initial decision, converging to a slower more accurate and controllable decision using software, programs, instructions, and the like readable and executable by processor 315. For example, if an interferer is detected, and processor 315 recognizes that the packet duration is longer than the wireless protocol will allow, AGC control circuits 303 and 304 and/or detector could be turned off by processor 315 to prevent signal transmission. Thus the normal AGC setting may be directly controlled and

overridden. Such control is further useful in situations including when a system feed-back oscillation is detected.

[0040] One of ordinary skill in the art will recognize that various techniques can be utilized to determine AGC set points as well as different signal detector configurations in the present invention. Additionally, various components, such as the gain control elements 305 and 306, AGC gain control 303 and 304, functionality of processor 315 and other elements could be combined into a single integrated device. Other changes and alterations to specific components, and the interconnections thereof, can be made by one of ordinary skill in the art without deviating from the scope and spirit of the present invention.

[0041] The invention has been described in detail with particular references to presently preferred embodiments thereof, but it will be understood that variations and modifications can be effected within the spirit and scope of the invention.

CLAIMS

What is claimed is:

1. A frequency translating repeater for use in a time division duplexing (TDD) radio protocol system, the frequency translating repeater comprising:
 - a detector circuit configured to detect if a signal is present on one of two frequency channels associated with the frequency translating repeater;
 - a frequency translator configured to change a frequency channel associated with the signal from the one of the two frequency channels to an other of the two frequency channels; and
 - a delay circuit configured to add a delay to the signal to compensate for a signal detection interval and a transmitter configuration interval.
2. The frequency translating repeater according to claim 1, wherein the delay circuit includes an analog storage device.
3. The frequency translating repeater according to claim 1, wherein the delay circuit includes at least one surface acoustic wave filter configured for one or more of: analog signal storage and channel selection.
4. The frequency translating repeater according to claim 1, wherein the detector circuit includes a processor.
5. The frequency translating repeater according to claim 4, wherein the detector circuit further includes an analog detection circuit.

6. The frequency translating repeater according to claim 1, further comprising a gain control circuit having one of a gain value and an attenuation value associated therewith.
7. The frequency translating repeater according to claim 6, wherein:
the detector is further for detecting a received signal strength of the signal, and
the gain control circuit is further for using the received signal strength of the signal to adjust a gain value of the signal.
8. The frequency translating repeater according to claim 7, wherein the gain control circuit is further for controlling at least one of the gain value and the attenuation value based on a predetermined criteria to achieve a specific signal transmit output power.
9. The frequency translating repeater according to claim 8, wherein the predetermined criteria is for modifying the specific signal transmit output power and includes at least one of the following: frequency separation between a receive frequency and a transmit frequency, a regulatory rule, a temperature, a received power level, a transmit power level, and a detected interference level.
10. The frequency translating repeater according to claim 8, wherein the processor further includes a memory and wherein the predetermined criteria are stored in the memory.

11. A frequency translating repeater for use in a time division duplexing (TDD) radio protocol system, the frequency translating repeater comprising:

a detector circuit configured to detect if a signal is present on one of two frequency channels associated with the frequency translating repeater and to detect a received detected signal power of the signal;

a frequency translator configured to change a frequency channel associated with the signal from the one of the two frequency channels to an other of the two frequency channels;

a delay circuit configured to add a delay to the signal to compensate for a signal detection interval and a transmitter configuration interval; and

a gain control circuit configured to adjust a gain value of the signal at least in part based on the received detected signal power detected by the detector circuit.

12. The frequency translating repeater according to claim 11, wherein the gain control circuit is further configured to adjust the gain value based at least in part on criteria including which of the one of the two frequency channels the signal is received on, and which of the other of the two frequency channels is changed to.

13. The frequency translating repeater according to claim 12, wherein the criteria further includes at least one of a regulatory rule for transmission, an operating temperature, and frequency separation between receive and transmit frequencies.

14. The frequency translating repeater according to claim 11, wherein the criteria further includes a distance between a receive frequency and a transmit frequency, and

wherein the automatic gain control circuit is further configured to apply more filtering to the signal based on the distance.

15. A frequency translating repeater for use in a time division duplexing (TDD) radio protocol system, the frequency translating repeater comprising:

a detector circuit configured to detect if a signal is present on one of two frequency channels associated with the frequency translating repeater;

a frequency converter configured to convert the signal from a radio frequency (RF) signal to an intermediate frequency (IF) signal;

a frequency translator configured to change a frequency channel associated with the IF signal from the one of the two frequency channels to an other of the two frequency channels;

a delay circuit configured to add a delay to the IF signal to compensate for a signal detection interval and a transmitter configuration interval; and

a gain control circuit configured to adjust a gain value of the IF signal.

16. The frequency translating repeater according to claim 15, wherein the gain control circuit is further configured to adjust the gain value of the IF signal at least in part based on a received detected signal power detected by the detector circuit.

17. The frequency translating repeater according to claim 15, wherein the detector circuit and the gain control circuit are located respectively on a first and a second signal path.

18. The frequency translating repeater according to claim 17, wherein the detector circuit includes a logarithmic amplifier and wherein the output of the logarithmic amplifier is coupled to the gain control circuit for control thereof.
19. The frequency translating repeater according to claim 18, wherein the detector circuit and the automatic gain control circuit each have different bandwidths.
20. The frequency translating repeater according to claim 19, wherein the automatic gain control circuit includes a processor and a memory storing a predetermined criteria and wherein the processor is configured to use the predetermined criteria to establish an offset gain value of the IF signal, resulting at least in part in a transmitter target output power independent of the detected receive power of the signal as detected by the detector circuit.
21. The frequency translating repeater according to claim 20, wherein processor is further configured to:
- convert the output of the logarithmic amplifier to a digital signal; and
 - establish the gain value of the IF signal using the digital signal.
22. A method for frequency translation in a frequency translating repeater for use in a time division duplexing (TDD) radio protocol system, the method comprising:
- a detecting if a signal is present on one of two frequency channels associated with the frequency translating repeater;
 - changing a frequency channel associated with the signal from the one of the two frequency channels to an other of the two frequency channels; and

adding a delay to the signal to equivalent to a signal detection interval and a transmitter configuration interval.

23. The method according to claim 22, wherein the adding the delay includes delaying the signal in an analog storage device.

24. The method according to claim 22, wherein the adding the delay includes at delaying the signal in at least one surface acoustic wave filter configured for one or more of: analog signal storage and channel selection.

25. The method according to claim 24, wherein the detecting includes detecting in an analog detection circuit.

26. The method according to claim 21, further comprising setting a gain associated with the signal.

27. The method according to claim 26, wherein the setting the gain further includes setting the gain in part based on a predetermined criteria.

28. The method according to claim 27, wherein the predetermined criteria includes at least one of the following: a distance between a receive frequency and a transmit frequency, a regulatory rule, a temperature, a received power level, a transmit power level, and a detected interference level.

29. The method according to claim 28, further comprising storing the predetermined criteria in a memory.

30. A method for frequency translation in a frequency translating repeater for use in a time division duplexing (TDD) radio protocol system, the method comprising:

detecting if a signal is present on one of two frequency channels associated with the frequency translating repeater;

changing a frequency channel associated with the signal from the one of the two frequency channels to an other of the two frequency channels;

adding a delay to the signal to compensate for a signal detection interval and a transmitter configuration interval; and

adjusting a gain value of the signal in part based on a detected receive power level of the signal.

31. The method according to claim 30, wherein the adjusting the gain value is based on a criteria including which of the one of the two frequency channels the signal is received on, and which of the other of the two frequency channels is changed to.

32. The method according to claim 30, wherein the criteria further includes a regulatory rule for transmission.

33. The method according to claim 31, wherein the criteria further includes frequency separation between a receive frequency and a transmit frequency.

34. A method for frequency translation in a frequency translating repeater for use in a time division duplexing (TDD) radio protocol system, the method comprising:

detecting if a signal is present on one of two frequency channels associated with the frequency translating repeater and, if so, a receive power level of the signal;

converting the signal from a radio frequency (RF) signal to an intermediate frequency (IF) signal;

changing a frequency channel associated with the IF signal from the one of the two frequency channels to an other of the two frequency channels;

adding a delay to the IF signal to compensate for a signal detection interval and a transmitter configuration interval; and

adjusting a gain value of the IF signal based at least in part on the detected receive power level of the signal.

35. The method according to claim 34, wherein the detecting and the adjusting are performed respectively on a first and a second signal path.

36. The method according to claim 35, wherein the detecting further includes generating a logarithmic signal from the signal and using the logarithmic signal for the adjusting.

37. The method according to claim 36, wherein the adjusting further includes using a predetermined criteria the adjusting the gain value of the IF signal.

38. The method according to claim 19, wherein the generating further includes converting the logarithmic signal to a digital signal; and wherein the adjusting further adjusting the gain value of the IF signal using the digital signal.

39. A frequency translating repeater for use in a time division duplexing communication system, the frequency translating repeater comprising:

- at least two receivers capable of receiving transmissions on at least first and second frequency channels;

- at least one transmitter capable of transmitting on the first frequency channel;

- at least one transmitter capable of transmitting on the second frequency channel;

- a detector circuit configured to detect if a signal is present on one of two frequency channels associated with the frequency translating repeater and for detecting a receive power level of the signal;

- a frequency translator configured to change a frequency channel associated with the signal from an initial one of the first and second frequency channels to a subsequent one of the first and second frequency channels;

- a microprocessor capable of configuring the first and second frequency channels based on pre-determined parameters stored therein, wherein

- configuration of a specific frequency for at least one of the first and second frequency channels is based on the pre-determined parameters, and

- the pre-determined parameters include at least one of the following: regulatory transmitter power limitations, regulatory out-of-band emissions limitations, and frequency separation between the first and second frequency channels.

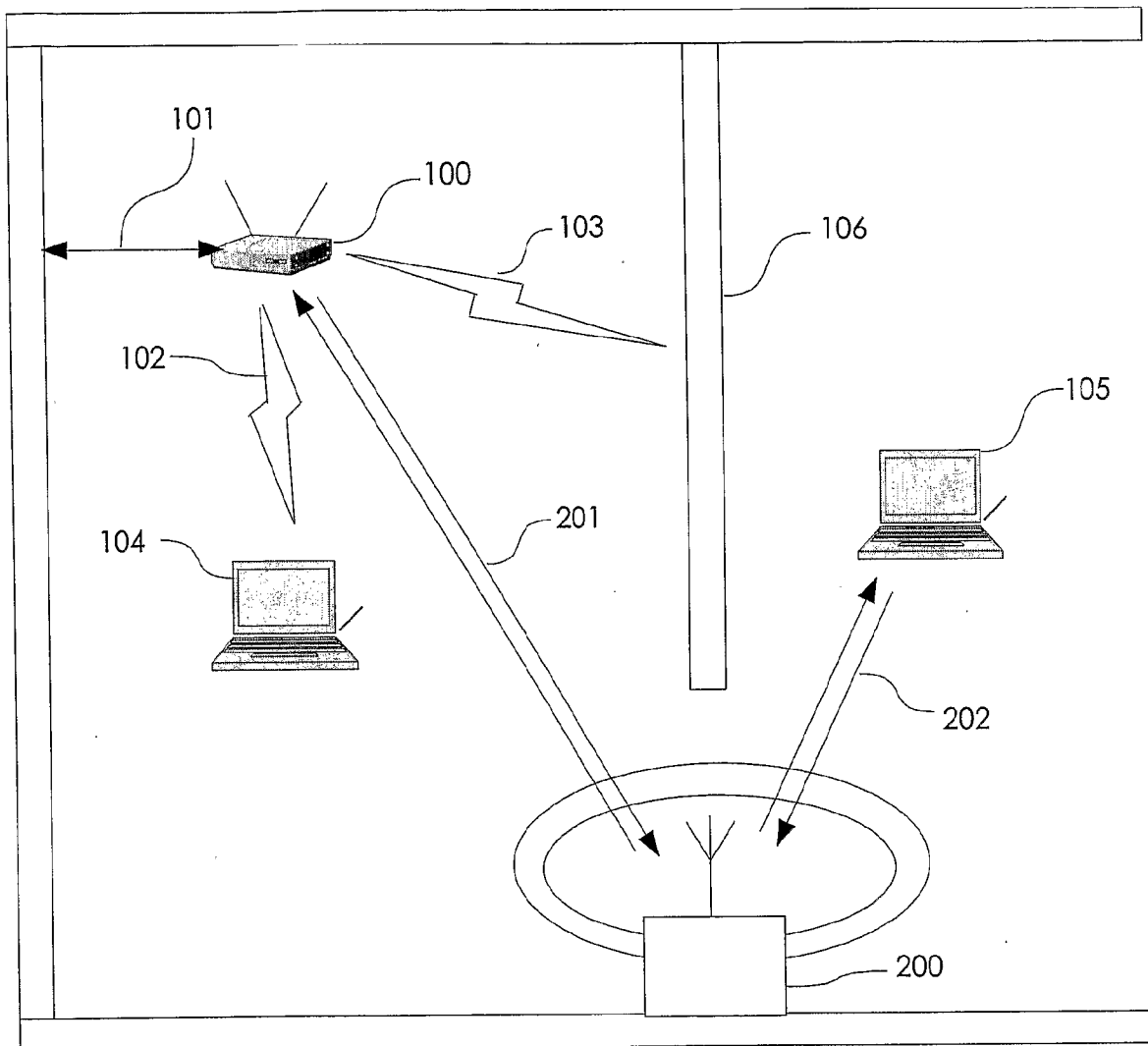


FIG. 1

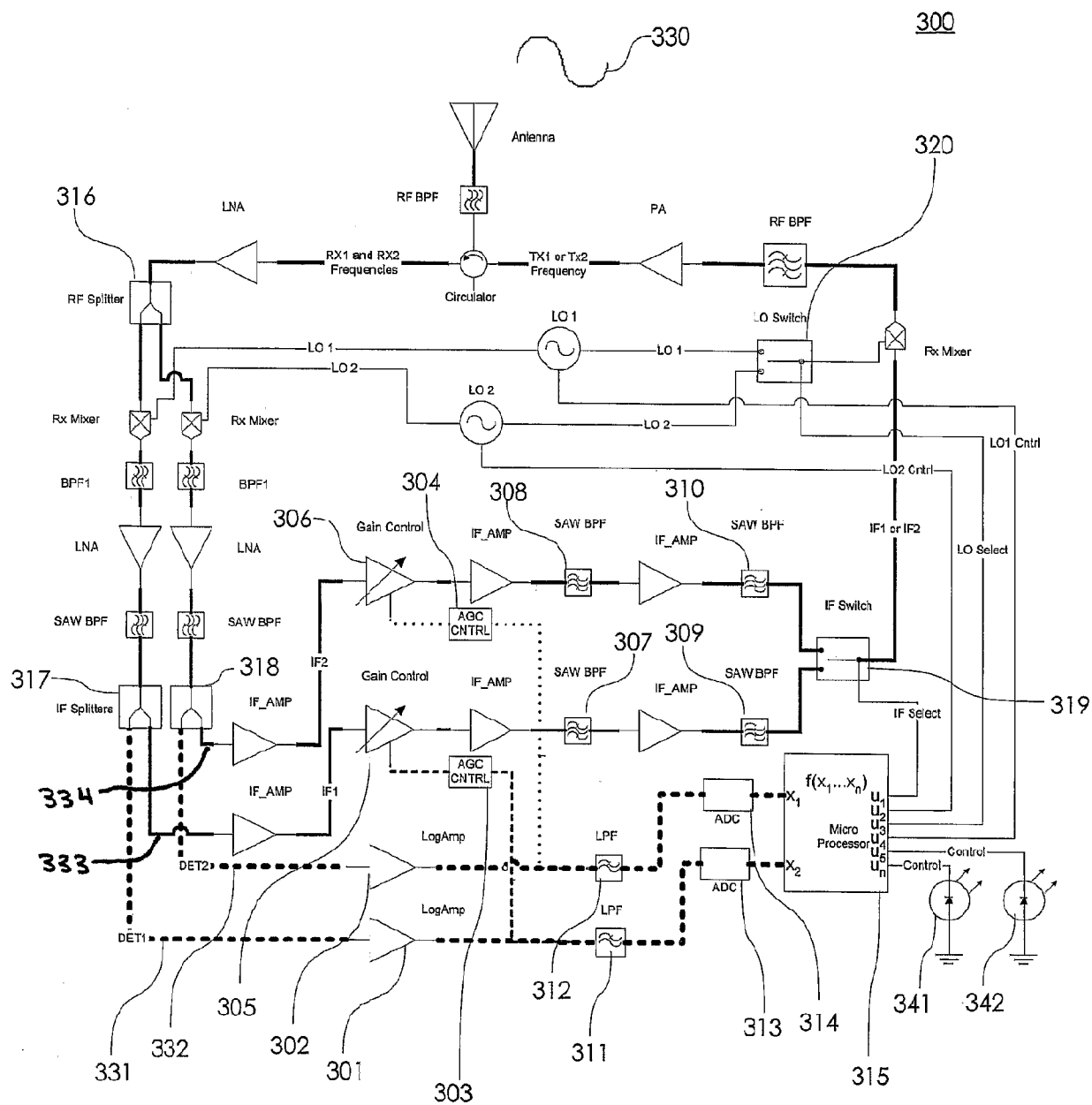


FIG. 2

INTERNATIONAL SEARCH REPORT

International application No.

PCT/US03/29130

A. CLASSIFICATION OF SUBJECT MATTER

IPC(7) : H04B 7/15

US CL : 455/11.1, 127.1, 127.2, 136, 138

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 455/11.1, 127.1, 127.2, 136, 138

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 6,404,775 B1 (LESLIE et al) 11 June 2002, see entire document.	1-39
A	US 5,726,980 A (RICKARD) 10 March 1998, see entire document.	1-39
A	US 2002/0109585 A1 (SANDERSON) 15 August 2002, see entire document.	1-39
A	US 2003/0185163 A1 (BERTONIS et al) 02 October 2003, see entire document.	1-39

☐ Further documents are listed in the continuation of Box C. ☐ See patent family annex.

* Special categories of cited documents:	
"A" document defining the general state of the art which is not considered to be of particular relevance	"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
"E" earlier application or patent published on or after the international filing date	"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
"O" document referring to an oral disclosure, use, exhibition or other means	"&" document member of the same patent family
"P" document published prior to the international filing date but later than the priority date claimed	

Date of the actual completion of the international search	Date of mailing of the international search report
07 January 2004 (07.01.2004)	21 JAN 2004
Name and mailing address of the ISA/US Mail Stop PCT, Attn: ISA/US Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450 Facsimile No. (703)305-3230	Authorized officer Edan Orgad Telephone No. 703-305-4223